



MEMC 96-0056(2101.3)
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of Robert Falster, et al.

Art Unit 1722

Serial No. 10/685,251

Filed October 14, 2003

Confirmation No. 3992

For PROCESS FOR PRODUCING LOW DEFECT DENSITY SILICON

EXAMINER Robert M. Kunemund

December 23, 2005

TO THE COMMISSIONER OF PATENTS AND TRADEMARKS,

SIR:

LETTER TO THE PATENT AND TRADEMARK OFFICE

In response to the Office action dated October 7, 2005,
please consider the following remarks.

Amendments to the Claims are reflected in the listing of
claims which begins on page 2.

Remarks begin on page 6.

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